

**AMENDMENTS TO THE CLAIMS**

Please **AMEND** claims 1-12, 18, 24-29, 31, 32 and 34-37, as shown below.

The following is a complete list of all claims in this application.

1. (Currently Amended) A ~~control-signal-part~~ wire arrangement, comprising:

a substrate; and

a plurality of wires formed the substrate, the plurality of wires comprising:

a first ~~signal~~ wire transmitting a first ~~signal~~ voltage and having a first end portion;

a second ~~signal~~ wire transmitting a second ~~signal~~ voltage smaller than the first  
~~signal~~ voltage and having a second end portion; and

a first redundancy wire ~~being~~ having a third end portion formed between the first  
~~signal-wire end portion~~ and the second ~~signal-wire end portion~~.

2. (Currently Amended) The ~~control-signal-part~~ wire arrangement of claim 1, the plurality of wires further comprising a second redundancy wire having a fourth end portion,  
wherein the first end portion is arranged between the third end portion and the fourth end portion  
~~the first redundancy wire being located at one side of the first signal wire and the second~~  
~~redundancy wire being located at the other side of the first signal wire.~~

3. (Currently Amended) The ~~control-signal-part~~ wire arrangement of claim 1,  
wherein the first redundancy wire is connected to the first signal wire.

4. (Currently Amended) The ~~control-signal-part~~ wire arrangement of claim 1, wherein the first redundancy wire is separated from the first ~~signal~~ wire.

5. (Currently Amended) The ~~control-signal-part~~ wire arrangement of claim 2, wherein at least one of the first redundancy wire and the second redundancy wire is connected to the first ~~signal~~ wire.

6. (Currently Amended) The ~~control-signal-part~~ wire arrangement of claim 2, wherein both the first redundancy wire and the second redundancy wire are separated from the first ~~signal~~ wire.

7. (Currently Amended) The ~~control-signal-part~~ wire arrangement of claim 1, wherein the first redundancy wire is formed by of a conductive material ~~materials having smaller less susceptible to oxidation than conductive materials for forming the second signal wire in a tendency to oxidation.~~

8. (Currently Amended) The ~~control-signal-part~~ wire arrangement of claim 7, wherein the first redundancy wire is formed by a conductive material ~~materials~~ selected from the group consisting of ~~one of~~ a copper family material, ~~one of~~ a silver family material, ~~one of~~ a chromium family material, and ~~one of~~ a molybdenum family material ~~comprising nitride chromium and nitride molybdenum.~~

9. (Currently Amended) The ~~control signal part~~ wire arrangement of claim 1, wherein the first redundancy wire is formed by a material ~~materials~~ selected from the group consisting of ITO and IZO.

10. (Currently Amended) The ~~control signal part~~ wire arrangement of claim 1, wherein the first redundancy wire transmits the same voltage as the first ~~signal~~ voltage.

11. (Currently Amended) The ~~control signal part~~ wire arrangement of claim 2, wherein the second redundancy wire transmits the same voltage as the first ~~signal~~ voltage.

12. (Currently Amended) A liquid crystal display comprising:  
a substrate;  
a display region including a gate line, a data line crossing the gate line thereby to define a pixel element region, a thin film transistor connected to the gate line and the data line in the pixel element region, and a pixel electrode electrically connected to the thin film transistor;  
a driving integrated circuit part including a gate driving integrated circuit for outputting a gate signal to the gate line and a data driving integrated circuit for outputting a data signal to the data line; and  
a control signal part for controlling the driving integrated circuit part, the control signal part including a plurality of wires comprises:  
a first signal wire transmitting a first signal voltage having a first end portion,  
a second signal wire transmitting a second signal voltage smaller than the first signal voltage and having a second end portion; and

a first redundancy wire having a third end portion ~~being~~ formed between the first ~~signal-wire end portion~~ and the second ~~signal-wire end portion~~.

13. (Original) The liquid crystal display of claim 12, wherein the control signal part further comprises signal wires for driving the gate driving integrated circuit.

14. (Original) The liquid crystal display of claim 12, wherein the control signal part further comprises signal wires for driving the data driving integrated circuit.

15. (Original) The liquid crystal display of claim 12, wherein the first signal voltage is a gate-on-voltage or a power supply voltage.

16. (Original) The liquid crystal display of claim 12, wherein the second signal voltage is a gate-off-voltage or a ground voltage.

17. (Original) The liquid crystal display of claim 12, wherein the first redundancy wire transmits the same voltage as the first signal voltage.

18. (Currently Amended) The liquid crystal display of claim 12, further comprising a second redundancy wire having a fourth end portion, wherein the first end portion is arranged between the third end portion and the fourth end portion ~~the first redundancy wire being located at one side of the first signal wire and the second redundancy wire being located at the other side of the first signal wire.~~

19. (Original) The liquid crystal display of claim 12, wherein the first redundancy wire is connected to the first signal wire.

20. (Original) The liquid crystal display of claim 12, wherein the first redundancy wire is separated from the first signal wire.

21. (Original) The liquid crystal display of claim 18, wherein the second redundancy wire transmits the same voltage as the first signal voltage.

22. (Original) The liquid crystal display of claim 18, wherein at least one of the first redundancy wire and the second redundancy wire is connected to the first signal wire.

23. (Original) The liquid crystal display of claim 18, wherein the first redundancy wire and the second redundancy wire are separated from the first signal wire.

24. (Currently Amended) The liquid crystal display of claim 12, wherein the first redundancy wire is formed by a conductive material less susceptible to oxidation than ~~oxidative conductive materials than conductive materials for forming~~ the second signal wire.

25. (Currently Amended) The liquid crystal display of claim 24, wherein the first redundancy wire is formed by of a ~~conductive materials~~ material selected from the group consisting of ~~one of~~ a copper family material, ~~one of~~ a silver family material, ~~one of~~ a chromium

family material, and ~~one of a~~ molybdenum family material ~~comprising nitride chromium and nitride molybdenum.~~

26. (Currently Amended) The liquid crystal display of claim 12, wherein the first redundancy wire is formed ~~by materials~~ of a material selected from the group consisting of ITO and IZO.

27. (Currently Amended) The liquid crystal display of claim 12, wherein the first redundancy wire is formed ~~by~~ of a conductive ~~materials~~ material for forming the gate line.

28. (Currently Amended) The liquid crystal display of claim 12, wherein the first redundancy wire is formed ~~by~~ of a conductive ~~materials~~ material for forming the data line.

29. (Currently Amended) The liquid crystal display of claim 12, wherein the first redundancy wire is formed ~~by~~ of a conductive ~~materials~~ material for forming the pixel electrode.

30. (Original) The liquid crystal display of claim 12, wherein the first signal wire has a wire structure in which a first wire is connected to a second wire, the first wire being connected to the gate driving integrated circuit and the second wire being connected to the data driving integrated circuit.

31. (Currently Amended) The liquid crystal display of claim 30, wherein the first wire is formed by of a conductive materials material for forming the gate line and the second wire is formed by of a conductive materials material for forming the data line.

32. (Currently Amended) The liquid crystal display of claim 30, wherein the first wire is formed by of a conductive materials material for forming the data line and the second wire is formed by of a conductive materials material for forming the gate line.

33. (Original) The liquid crystal display of claim 12, wherein the first redundancy wire has a wire structure in which a first wire is connected to a second wire, the first wire being connected to the gate driving integrated circuit and the second wire being connected to the data driving integrated circuit.

34. (Currently Amended) The liquid crystal display of claim 33, wherein the first wire is formed by of a conductive materials material for forming the gate line and the second wire is formed by of a conductive materials material for forming the data line.

35. (Currently Amended) The liquid crystal display of claim 33, wherein the first wire is formed by of a conductive materials material for forming the data line and the second wire is formed by of a conductive materials material for forming the gate line.

36. (Currently Amended) The liquid crystal display of claim 12, wherein the signal wires of the control signal part include:

a lower wire having a pad and ~~being~~ formed ~~by~~ of a conductive ~~materials~~ material for forming the gate line;<sub>i</sub>

a first insulating layer covering the lower wire;<sub>i</sub>

a first contact hole exposing one end of the lower wire;<sub>i</sub> and

an upper wire having a pad and being formed ~~by~~ of a conductive ~~materials~~ material for forming the data line, the upper wire ~~being~~ connected to the lower wire through the first contact hole.

37. (Currently Amended) The liquid crystal display of claim 36, further comprising:

a second insulating layer covering the upper wire;<sub>i</sub>

a second contact hole exposing the pad of the upper wire;<sub>i</sub> and

a third contact hole exposing the pad of the lower wire;<sub>i</sub> and

an auxiliary pad covering and ~~being~~ connected to the pads of the lower and the upper wires through the second and third contact holes.